SigmaDiff: Semantics-Aware Deep Graph Matching for Pseudocode Diffing

Lian Gao, Yu Qu, Sheng Yu, Yue Duan, Heng Yin

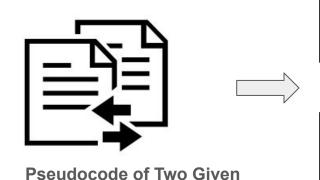








Pseudocode of Two Given Binaries

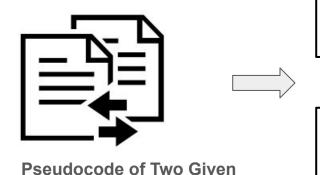


Binaries

Locate Similar Tokens

Capture
Different
Tokens



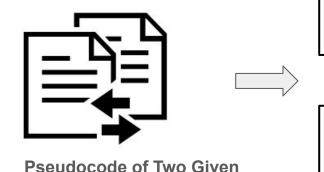


Binaries

Locate Similar Tokens

Capture Different Tokens

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Binaries

Locate Similar Tokens

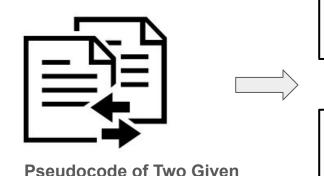
Capture Different Tokens



Vulnerability/patch Detection



Plagiarism Detection



Binaries

Locate Similar Tokens

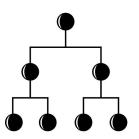
Capture Different Tokens



Vulnerability/patch Detection



Plagiarism Detection



Lineage Analysis

Benefits:

More concise and human-readable

```
size t FUN 00403a80(void *param 1, size t param 2) {
            void *pvVar1;
           pvVar1 = memchr(param 1,0,param 2);
           if (pvVar1 != (void *)0x0) {
                param 2 = (long)pvVar1 + (1 - (long)param 1);
                return (long)pvVar1 + (1 - (long)param 1);
6.
7.
           return param 2;
                                                                                             inserted
                                (a) Pseudocode Diffing
                                                                                             deleted
                00403A80
                        sub_00403A80
                                                    00403850
                                                             sub_00403B50
                00403A80
                        push
                                                    00403850
                                                             push
                00403A81
                        push
                                  rbx
                                                    00403B51
                                                                     rbx
                00403A82
                                  rsp, b1 8
                00403A86
                        mov
                                  rbx, rdi
                                                    00403852
                00403489
                                  rbp, rsi
                                                                     rdx, rsi
                00403A8C
                                  rdx, rsi
                                                    00403855
                                                                     rbp, rsi
                                                    00403858
                                                                     b4 esi, b4 esi // c
                00403A8F
                                  b4 esi, b4 0 // c
                                                    00403854
                                                    00403B5D
                                                                     rsp, b1 8
                00403A94
                                   memchr
                                                    00403861
                                                            call
                                                                      memchr
                00403A99
                                                    00403B66
                                  0x403AA7
                                                                     0x403B80
                00403A9C
                                                    00403B69
                                                                                  88483858
                                                                                          sub_00403B50
98483A9E
                                                       00403850
        sub
                  rax, rbx
                            00403A80
                                    sub_00403A80
                                                      00403B6B
                                                                                 00403B80
                                                                                          add
                                                                                                  rsp, b1 8
                                                               sub
                                                                        rax, rbx
00403AA1
                            00403AA7
                                              rax, rbs
                                                                                 00403B84
00403AA5 imp
                  Өх4ӨЗААА
                                                       00403B6F
                                                                        rsp, b1 8
                                                                                          mov
                                                                                                  rax, rbp
                                                                        rax, bi
                                                                                  00403887
                                                                                                  грх
                                                                                  00403888
                                                                                                  rbp
                                                       00403B76
                                                                        rbx
                                                                                          pop
                                                      00403877
                                                                        rbp
                                                                                 00403B89
                                                      00403B78
                  00403A80
                  88483AAA
                          add
                                    rsp, b1
                  00403AAE
                  00403AAF
                                    rbp
                  00403AB0
                                 (b) Binary Diffing
```

Benefits:

- More concise and human-readable
- More fine-grained

```
size t FUN 00403a80(void *param 1, size t param 2) {
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           pvVar1 = memchr(param 1,0,param 2);
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6.
7.
           return param 2;
                                                                                             inserted
                                (a) Pseudocode Diffing
                                                                                            deleted
                                                             sub_00403B50
                00403A80
                        sub_00403A80
                                                    00403850
                00403A80
                        push
                                                    00403850
                                                             push
                00403A81
                        push
                                  rbx
                                                    00403B51
                                                                     rbx
                00403A82
                                  rsp, b1 8
                00403A86
                                  rbx, rdi
                                                    00403852
                00403489
                                  rbp, rsi
                                                                     rdx, rsi
                00403A8C
                                                    00403855
                                                                     rbp, rsi
                                                    00403858
                                                                     b4 esi, b4 esi // c
                00403A8F
                                  b4 esi, b4 0 // c
                                                    00403854
                                                    00403B5D
                                                                     rsp, b1 8
                00403A94
                                   memchr
                                                    00403861
                                                                      memchr
                00403A99
                                                    00403B66
                                  0x403AA7
                00403A9C
                                                    00403B69
                                                                     0×403B80
                                                                                 88483858
                                                                                          sub_00403B50
98483A9E
                                                       00403850
        sub
                  rax, rbx
                            00403A80
                                    sub_00403A80
                                                      00403B6B
                                                                                 00403B80
                                                                                          add
                                                                                                  rsp, b1 8
                                                               sub
                                                                       rax, rbx
00403AA1
                            00403AA7
                                              rax, rbs
                                                                                 00403B84
00403AA5 imp
                  Өх4ӨЗААА
                                                       00403B6F
                                                                        rsp, b1 8
                                                                                          mov
                                                                                                  rax, rbp
                                                                        rax, bi
                                                                                 00403887
                                                                                                  грх
                                                                                  00403888
                                                                                                  rbp
                                                       00403B76
                                                                        rbx
                                                                                          pop
                                                      00403877
                                                                        rbp
                                                                                 00403B89
                                                      00403B78
                  00403A80
                  88483AAA
                          add
                                    rsp, b1
                  00403AAE
                  00403AAF
                  00403AB0
                                 (b) Binary Diffing
```

Benefits:

- More concise and human-readable
- More fine-grained
- The recovered semantic information could be leveraged

```
size t FUN 00403a80(void *param 1, size t param 2) {
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            if (pvVar1 != (void *)0x0) {
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6.
7.
            return param 2;
                                                                                             inserted
                                (a) Pseudocode Diffing
                                                                                             deleted
                        sub_00403A80
                00403A80
                                                    00403850
                                                             sub 00403B50
                00403A80
                         push
                                                    00403850
                00403A81
                         push
                                  rbx
                                                    00403851
                                                                     rbx
                00403A82
                                  rsp, b1 8
                00403A86
                                  rbx, rdi
                00403489
                                                    00403852
                                                                     rdx, rsi
                                  rbp, rsi
                00403A8C
                                                    00403855
                                                                     rbp, rsi
                                                    00403858
                                                                     54 esi, 54 esi // c
                00403A8F
                                  b4 esi, b4 0 // c
                                                    00403854
                                                    88483B5D
                                                                     rsp, b1 8
                00403A94
                                   memchi
                                                    00403861
                                                                      memchr
                00403A99
                                                    00403B66
                                  0x403AA7
                00403A9C
                                                    00403B69
                                                                     0×403B80
                                                                                  88483858
                                                                                          sub_00403B50
98483A9E
                                                       00403850
                  rax, rbx
                            00403A80
                                    sub_00403A80
                                                      00403B6B
                                                                                 00403B80
                                                                                          add
                                                                                                  rsp, b1 8
                                                                        rax, rbx
00403AA1
                            00403AA7
                                                                                 00403B84
                                                       00403B6F
                                                                        rsp, b1 8
                                                                                          mov
                                                                                                  rax, rbp
00403AA5
                  Өх4ӨЗААА
                                                                        rax, bi
                                                                                  00403887
                                                                                                  rbx
                                                                                  00403888
                                                       00403B76
                                                                        rbx
                                                                                          DOD
                                                                                                  rbp
                                                      88483877
                                                                        rbp
                                                                                  00403B89
                  00403A80
                  88483AAA
                          add
                                    rsp, b1
                  00403AAE
                  00403AAF
                  00403AB0
                                 (b) Binary Diffing
```

Benefits:

- More concise and human-readable
- More fine-grained
- The recovered semantic information could be leveraged
- Natural support of cross-architecture diffing

```
size_t FUN_00403a80(void *param_1, size_t param_2) {
           void *pvVar1;
           pvVar1 = memchr(param 1,0,param 2);
           if (pvVar1 != (void *)0x0) {
                param 2 = (long)pvVar1 + (1 - (long)param 1);
                return (long)pvVar1 + (1 - (long)param 1);
6.
7.
           return param 2;
                                                                                           inserted
                               (a) Pseudocode Diffing
                                                                                           deleted
                        sub_00403A80
                00403A80
                                                    00403850
                                                            sub 00403B50
                00403A80
                                                   00403850
                00403A81
                                 rbx
                                                    00403851
                                                                    rbx
                00403A82
                                 rsp, b1 8
                00403A86
                00403489
                                                   00403852
                00403A8C
                                                   00403855
                                                                    rbp, rsi
                                                    00403858
                                                                    54 esi, 54 esi // c
                00403A8F
                                  b4 esi, b4 0 // c
                                                   00403854
                                                                    rsp, b1 8
                00403A94
                                  memchi
                                                   00403861
                                                                     memchr
                00403A99
                                                   00403B66
                                 0x403AA7
                00403A9C
                                                   00403B69
                                                                                88483858
                                                                                        sub 88483858
99493A9E
                                                      00403850
                  rax, rbx
                                   sub_00403A80
                                                     00403B6B
                                                                                00403B80
                                                                                        add
                                                                                                 rsp, b1 8
                                                                       rax, rbx
00403AA1
                           00403AA7
                                                                                00403B84
                                                      00403B6F
                                                                       rsp, b1 8
                                                                                        mov
                                                                                                 rax, rbp
00403AA5
                  Өх4ӨЗААА
                                                                       rax, bi
                                                                                00403887
                                                                                                 rbx
                                                      00403B76
                                                                       rbx
                                                                                00403888
                                                                                        pop
                                                                                                 rbp
                                                     88483877
                                                                       rbp
                                                                                00403B89
                 88483444
                          add
                                   rsp, b1
                  BB4B3AAF
                  00403AAF
                  00403AB0
                                (b) Binary Diffing
```

Existing Work

Traditional approaches (e.g., BinDiff, Diaphora)

- rely on heuristics (e.g., function name) to find similar functions, perform basic block matching along the control flow graph (BinDiff), or simple string-based matching at the token level (Diaphora)
- not robust and can be easily thwarted by compiler optimizations

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Dynamic analysis-based approaches (e.g., BLEX, iBinHunt)

- capturing the semantics of binaries and have good resilience against code obfuscation
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Learning-based approaches (e.g., DeepBinDiff)

- encode graph information into numerical vectors, a.k.a, graph embeddings, and perform binary diffing
- distill unique semantic-level features of a program
- does not scale well on large binaries

Challenges:

Pseudocode-level changes (noises) that are caused by compilers and decompilers

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Source Code

Pseudocode (old version)

```
1.
     uVar13 = 0xff;
                                             inserted
     if (0xfe < uVar12) {
                                              deleted
       uVar13 = (ulong)uVar12;}
3.
                                             moved
     s = (void *)(**(code
     **)param_1[1])(param_1,1,uVar13 + 1);
                                             updated
      .....
     do {
       *(char *)(*(long *)(param_2 + 0x48) +
     lVar14) = (char)((long)uVar13 /
     (long)(ulong)uVar12);
       lVar14 = lVar14 + 1;
       uVar13 = uVar13 + 0xff;
     } while (lVar14 != (ulong)uVar12 + 1);
```

Challenges:

Pseudocode-level changes (noises) that are caused by compilers and decompilers

Different variable names

Source Code

Pseudocode (old version)

```
1.
     uVar13 = 0xff;
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     (long)(ulong)uVar12);
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Challenges:

Pseudocode-level changes (noises) that are caused by compilers and decompilers

- Different variable names
- Different expressions

Source Code

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```
1.
     uVar13 = 0xff;
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     s = (void *)(**(code
     **)param_1[1])(param_1,1,uVar13 + 1);
                                             updated
      .....
     do {
       *(char *)(*(long *)(param_2 + 0x48) +
     IVar14) = (char)((long)uVar13 /
     (long)(ulong)uVar12);
       lVar14 = lVar14 + 1;
7.
       uVar13 = uVar13 + 0xff;
     } while (lVar14 != (ulong)uVar12 + 1);
```

Challenges:

Pseudocode-level changes (noises) that are caused by compilers and decompilers

- Different variable names
- Different expressions
- Different control constructs

Source Code

Pseudocode (old version)

```
1.
     uVar13 = 0xff;
                                             inserted
     if (0xfe < uVar12) {
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                                             moved
     s = (void *)(**(code
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      .....
    do {
       *(char *)(*(long *)(param 2 + 0x48) +
     lVar14) = (char)((long)uVar13 /
     (long)(ulong)uVar12);
7.
       lVar14 = lVar14 + 1;
       uVar13 = uVar13 + 0xff;
    } while (lVar14 != (ulong)uVar12 + 1);
```

Observations:

 A large number of syntax level changes in decompilation are introduced during the transformation from intermediate representation (IR) to pseudocode

Design Choice:

 Perform diffing at IR level and map the IR-level diffing results up to the pseudocode level

Source Code

Pseudocode (old version)

```
1.
     uVar13 = 0xff;
                                              inserted
     if (0xfe < uVar12) {
                                              deleted
       uVar13 = (ulong)uVar12;}
3.
                                              moved
     _s = (void *)(**(code
      **)param 1[1])(param 1,1,uVar13 + 1);
                                              updated
     do {
        *(char *) (*(long *)(param_2 + 0x48) +
     IVar14) = (char)((long)uVar13 /
     (long)(ulong)uVar12);
       lVar14 = lVar14 + 1;
7.
       uVar13 = uVar13 + 0xff;
     } while (lVar14 != (ulong)uVar12 + 1);
```

Challenges:

Different variable names

Design Choice:

 Perform a lightweight symbolic analysis to associate each IR variable with a symbolic expression that reveals how the value of that IR variable is calculated

Source Code

Pseudocode (old version)

```
1.
     uVar13 = 0xff;
                                              inserted
2.
     if (0xfe < uVar12) {
                                              deleted
       uVar13 = (ulong)uVar12;}
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                                              moved
     _s = (void *)(**(code
      **)param 1[1])(param 1,1,uVar13 + 1);
                                              updated
     do {
        *(char *)(*(long *)(param 2 + 0x48) +
     lVar14) = (char)((long)uVar13 /
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       lVar14 = lVar14 + 1;
7.
       uVar13 = uVar13 + 0xff;
     } while (lVar14 != (ulong)uVar12 + 1);
```

Challenges:

Unstable control flow

Design Choice:

 Leverage data and control dependencies to capture contextual information for each IR

Source Code

Pseudocode (old version)

```
uVar13 = 0xff;
1.
                                             inserted
     if (0xfe < uVar12) {
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     _s = (void *)(**(code
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    do {
       *(char *)(*(long *)(param 2 + 0x48) +
     lVar14) = (char)((long)uVar13 /
     (long)(ulong)uVar12);
       lVar14 = lVar14 + 1;
7.
       uVar13 = uVar13 + 0xff;
8.
    } while (lVar14 != (ulong)uVar12 + 1);
```

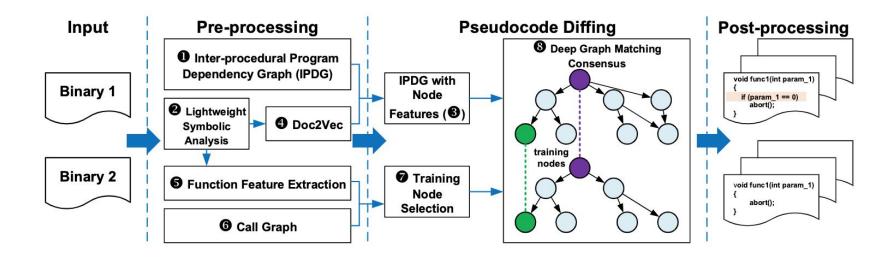
Challenges

- Noises in the graph matching
- Large number of tokens
- NP-hard graph matching problem

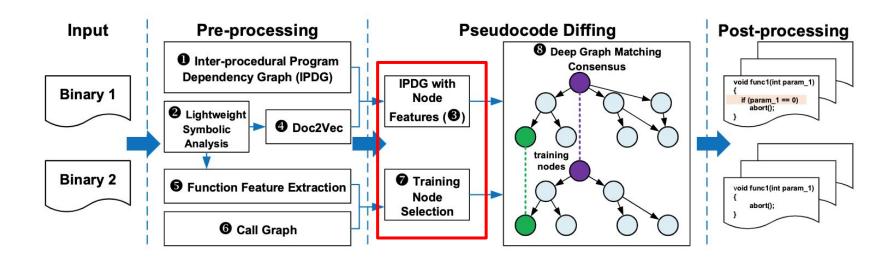
Design Choice

- Apply deep graph matching consensus (DGMC) model to fully exploit the neighboring contextual information
- Leverages the computing power of modern GPUs

Approach Overview



Approach Overview

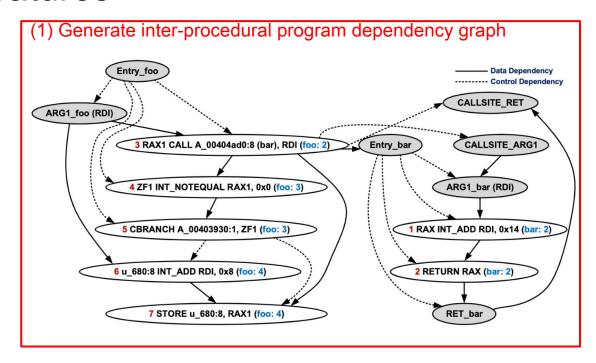


IPDG with Node Features

```
1. void foo(undefined8* param_1) {
2.    lVar2 = bar(param_1);
3.    if (lVar2 != 0) {
4.        *(param_1 + 8) = lVar2;
5.    }
6. }
1. undefined8* bar(undefined8* param_1){
2.    return param_1 + 20;
3. }
```

IPDG with Node Features

```
1. void foo(undefined8* param_1) {
2.    lVar2 = bar(param_1);
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1. undefined8* bar(undefined8* param_1){
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```

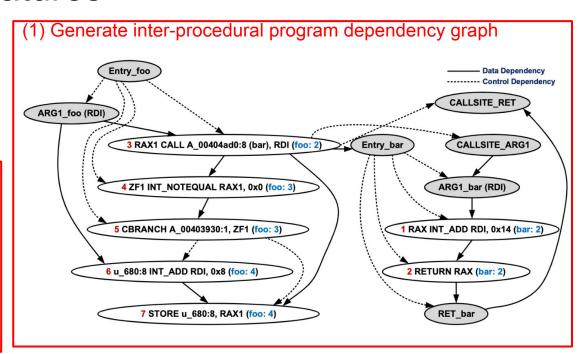


IPDG with Node Features

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2.     lVar2 = bar(param_1);
3.     if (lVar2 != 0) {
4.         *(param_1 + 8) = lVar2;
5.     }
6.     }
1. undefined8* bar(undefined8* param_1){
2.     return param_1 + 20;
3. }
```

ID	Symbolic Analysis Results	Node Features
1	$\alpha(RDI) := ARG1$ $\alpha(RAX) := ARG1 + 20$	ARG1 + 20 INT_ADD ARG1 20
2	N/A	RETURN ARG1 + 20
3	$\alpha(RDI) := ARG1$ $\alpha(RAX1) := ARG1 + 20$	ARG1 + 20 CALL ADDR ARG1
4	a(ZF1) := 0/1	0 INT_NOTEQUAL ARG1 +20 CONST
5	N/A	CBRANCH ARG1 + 20 CONST
6	$\alpha(u_680:8) := ARG1 + 8$	ARG1 + 8 INT_ADD ARG1 CONST
7	$\alpha([ARG1 + 8]) := ARG1 + 20$	STORE CONST ARG1 + 8 ARG1 + 20

- (2) Perform lightweight symbolic analysis
- => Node Features



Training Node Selection

Training Nodes:

Node pairs with high similarity and uniqueness

Used in semi-supervised learning:

Source and target graphs + small
 set of training nodes => mappings for the rest of the nodes

Training Node Selection

Select IRs (nodes):

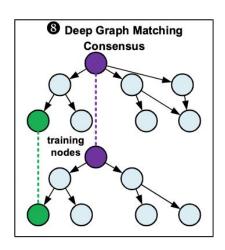
- that have unique node feature
- that appear in both graphs

```
void foo(undefined8* param_1) {
     void foo(undefined8* param_1) { 1.
                                               lVar2 = bar(param_1);
2.
       1Var2 = bar(param_1);
                                               if (1Var2 == 0) {
       if (1Var2 != 0) {
                                       4.
                                                 return;
4.
         *(param 1 + 8) = 1Var2;
                                       5.
5.
                                       6.
                                               *(param_1 + 8) = 1Var2;
6.
```

STORE CONST ARG1+8 ARG1+20

ID	Symbolic Analysis Results	Node Features
1	α(RDI) := ARG1	ARG1 + 20 INT_ADD ARG1 20
	$\alpha(RAX) := ARG1 + 20$	
2	N/A	RETURN ARG1 + 20
3	$\alpha(RDI) := ARG1$ $\alpha(RAX1) := ARG1 + 20$	ARG1 + 20 CALL ADDR ARG1
4	$\alpha(ZF1) := 0/1$	0 INT_NOTEQUAL ARG1 +20 CONST
5	N/A	CBRANCH ARG1 + 20 CONST
6	$\alpha(u 680:8) := ARG1 + 8$	ARG1 + 8 INT_ADD ARG1 CONST
7	$\alpha([ARG1 + 8]) := ARG1 + 20$	STORE CONST ARG1 + 8 ARG1 +20

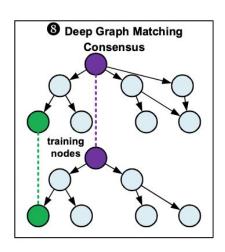
Diffing



Deep Graph Matching Consensus (DGMC) model [1]

- A two-stage deep graph matching architecture
 - Leveraging Graph Neural Network (GNN) to obtains the initial matchings
 - Iteratively reaching neighborhood consensus (ensuring the neighbors of the matched nodes are correctly matched to each other as well)

Diffing



Deep Graph Matching Consensus (DGMC) model [1]

- A two-stage deep graph matching architecture
 - Leveraging Graph Neural Network (GNN) to obtains the initial matchings
 - Iteratively reaching neighborhood consensus (ensuring the neighbors of the matched nodes are correctly matched to each other as well)

Modifications:

- Increase the penalty of incompatible types
- Add more hops
- Introduce the "pre-training and fine-tuning" schema
- Design an iterative algorithm to avoid the out-of-memory problem in GPU

^[1] Fey, Matthias, et al. "Deep Graph Matching Consensus." *International Conference on Learning Representations*. 2019.

- Compare with Diaphora and DeepBinDiff
 - Cross-version
 - Cross-optimization-level
 - Cross-compiler
 - Cross-architecture
- Conduct Patch Detection
 - Case studies on real-world vulnerabilities
 - Zoom

TABLE V: Cross-version Pseudocode Diffing Results. Si: SIGMADIFF, De: DeepBinDiff, Di: Diaphora

		Recall Precision					F1			
		Si	De	Di	Si	De	Di	Si	De	Di
500	v5.93 - v8.1	0.624	0.589	0.438	0.770	0.743	0.759	0.687	0.654	0.549
Coreutils	v6.4 - v8.1	0.689	0.631	0.551	0.793	0.767	0.643	0.735	0.691	0.592
	Average	0.656	0.610	0.494	0.781	0.755	0.701	0.711	0.673	0.571
	v2.8 - v3.6	0.694	0.660	0.348	0.848	0.806	0.406	0.763	0.725	0.375
Diffutils	v3.4 - v3.6	0.928	0.909	0.788	0.957	0.960	0.844	0.942	0.934	0.815
,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	Average	0.811	0.784	0.568	0.903	0.883	0.625	0.853	0.829	0.595
60 DE 8001	v4.233 - v4.6	0.685	0.579	0.366	0.825	0.758	0.773	0.748	0.655	0.487
Findutils	v4.41 - v4.6	0.769	0.690	0.499	0.868	0.847	0.786	0.814	0.759	0.609
	Average	0.727	0.635	0.433	0.847	0.803	0.779	0.781	0.707	0.548
	v6.0.0 - v6.2.1	0.815	N/A	0.691	0.871	N/A	0.892	0.842	N/A	0.779
Gmp	v6.1.1 - v6.2.1	0.858	N/A	0.771	0.894	N/A	0.920	0.876	N/A	0.839
	Average	0.836	N/A	0.731	0.882	N/A	0.906	0.859	N/A	0.809
	v0.75 - v0.77	0.781	N/A	0.741	0.899	N/A	0.897	0.836	N/A	0.812
Putty	v0.76 - v0.77	0.798	N/A	0.732	0.908	N/A	0.881	0.849	N/A	0.800
7.0	Average	0.789	N/A	0.737	0.904	N/A	0.889	0.842	N/A	0.806

TABLE VII: Cross-architecture Pseudocode Diffing Results (ARM vs. x86-64). Si: SIGMADIFF, Di: Diaphora

	Recall		Prec	ision	F1		
	Si	Di	Si	Di	Si	Di	
Coreutils 8.1	0.720	0.429	0.725	0.954	0.723	0.586	
Diffutils 3.6	0.751	0.003	0.754	0.674	0.752	0.006	
Findutils 4.6	0.620	0.003	0.642	0.521	0.631	0.006	
Gmp 6.2.1	0.362	0.165	0.386	0.827	0.373	0.275	
Putty 0.76	0.143	0.038	0.436	0.591	0.216	0.072	

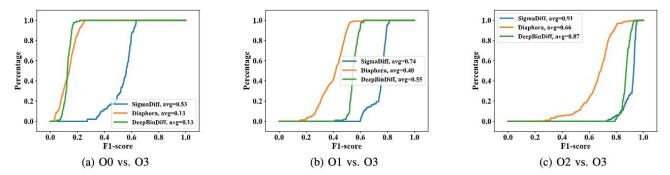
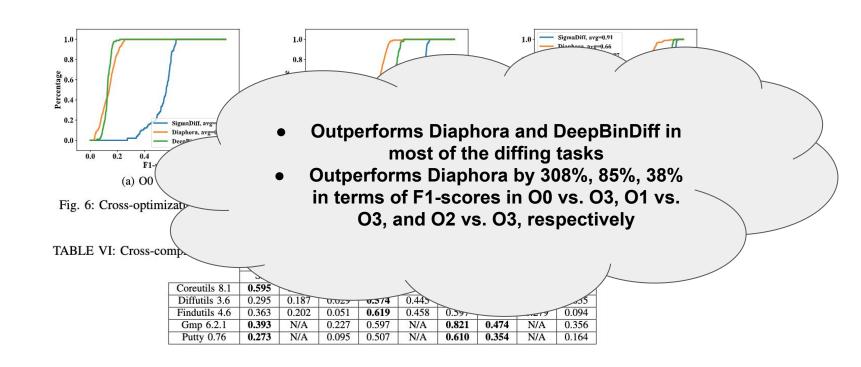


Fig. 6: Cross-optimization-level Pseudocode Diffing F1-score CDF (merging results of Coreutils, Diffutils, and Findutils)

TABLE VI: Cross-compiler Pseudocode Diffing Results (Clang vs. GCC). Si: SIGMADIFF, De: DeepBinDiff, Di: Diaphora

	Recall				Precision	la j	F1			
	Si	De	Di	Si	De	Di	Si	De	Di	
Coreutils 8.1	0.595	0.203	0.262	0.807	0.482	0.720	0.681	0.285	0.382	
Diffutils 3.6	0.295	0.187	0.029	0.574	0.445	0.535	0.390	0.263	0.055	
Findutils 4.6	0.363	0.202	0.051	0.619	0.458	0.597	0.457	0.279	0.094	
Gmp 6.2.1	0.393	N/A	0.227	0.597	N/A	0.821	0.474	N/A	0.356	
Putty 0.76	0.273	N/A	0.095	0.507	N/A	0.610	0.354	N/A	0.164	



Vulnerability/Patch Analysis

TABLE XI: Diffing results for Zoom. Si: SIGMADIFF, Di: Diaphora, Bi: BinDiff

Library	Ver.	Confirmed	Fu	nction	-lvl	Token-lvl			
Library	ver.	CVEs	Si	Di	Bi	Si	Di	Bi	
		CVE-2023-0464	✓		✓	V			
		CVE-2023-0215	✓						
O===00I	1 1 11.	CVE-2022-4450	✓	✓		✓			
OpenSSL	1.1.1k	CVE-2022-0778	✓	✓	✓	✓		✓	
		CVE-2021-3712	✓	✓		✓			
		CVE-2021-3711	✓			✓			
COI :ta	3.33.0	CVE-2022-35737	√						
SQLite	3.33.0	CVE-2021-20227	✓						
masimma aata	1.11	CVE-2021-3672	√	√					
resiprocate	1.11	CVE-2017-9454	✓	✓					
libjpeg-turbo	2.0.4	CVE-2020-13790	√	√	√	√	√	✓	
EEmnag	4.2.3	CVE-2021-38291	√	√	√	V	✓	√	
FFmpeg	4.2.3	CVE-2020-22037	✓	✓	✓	✓			

Vulnerability/Patch Analysis

TABLE XI: Diffing results for Zoom. Si: SIGMADIFF, Di:

Diaphora, Bi: BinDiff

Library	Ver.	Confirmed	Fu	nction	-lvl	Token-lvl				
Library	ver.	CVEs	Si	Di	Bi	Si	Di	Bi		
OpenSSL	1.1.1k	LCVI		•					lities in	
SQLite	3.33.0	•		.6.22 isely	,	ooin	t eia	ht		
resiprocate	1.11	y		erabi			_		/el	
libjpeg-turbo	2.0.4	CVE-202								
FFmpeg	4.2.3	CVE-2021-382>1 CVE-2020-22037	V	V	\	V				

Open Source Project

https://github.com/yijiufly/SigmaDiff

Thank you!