LLMPirate: LLMs for Black-box Hardware IP Piracy

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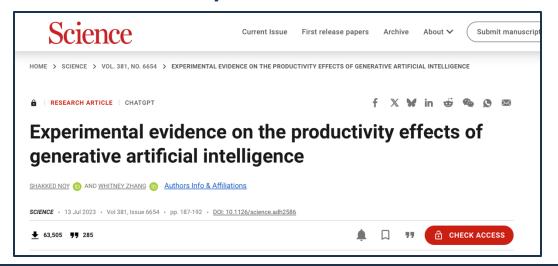
Joey See

Jeyavijayan Rajendran





LLMs are Everywhere



Education

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Al+Education: How Large Language Models Could Speed Promising New Classroom Curricula

Stanford computer science scholars propose using language models to create new learning materials for K-12 students.

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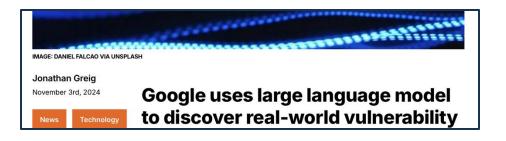
Bloomberg Professional Services ——

Introducing BloombergGPT, Bloomberg's 50-billion parameter large language model, purpose-built from scratch for finance

SemiKong is the world's first open-source semiconductor-focused LLM — it claims to bring new chips to market 30% faster

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By Dallin Grimm published December 28, 2024





LLM-related Attack Vectors

How LLMs Are Powering Next-Gen Malware: The New Cyber Frontier

SEAN GRIMALDI — NOVEMBE Veracode highlights security risks of GenAl coding tools

At Black Hat USA 2024, Veracode's Chris Wysopal warned of the downstream effects of how generative AI tools are helping developers write code faster.





Bv Arielle Waldman, News Writer

Published: 07 Aug 2024

Are you 99 99% confident

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Researchers Highlight How Poisoned LLMs Can Suggest **Vulnerable Code**

CodeBreaker technique can create code samples that poison the output of code-completing large language models, resulting in vulnerable — and undetectable — code suggestions.



Robert Lemos, Contributing Writer

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LLM-related Attack Vectors

How LLMs Are Powering Next-Gen

Can LLMs Successfully Pirate Hardware Intellectual Property (IP)?

At Black Hat USA 2024, Veracode's Chris Wysopal warned of the downstream effects of how generative Al tools are helping developers write code faster.





By Arielle Waldman, News Writer

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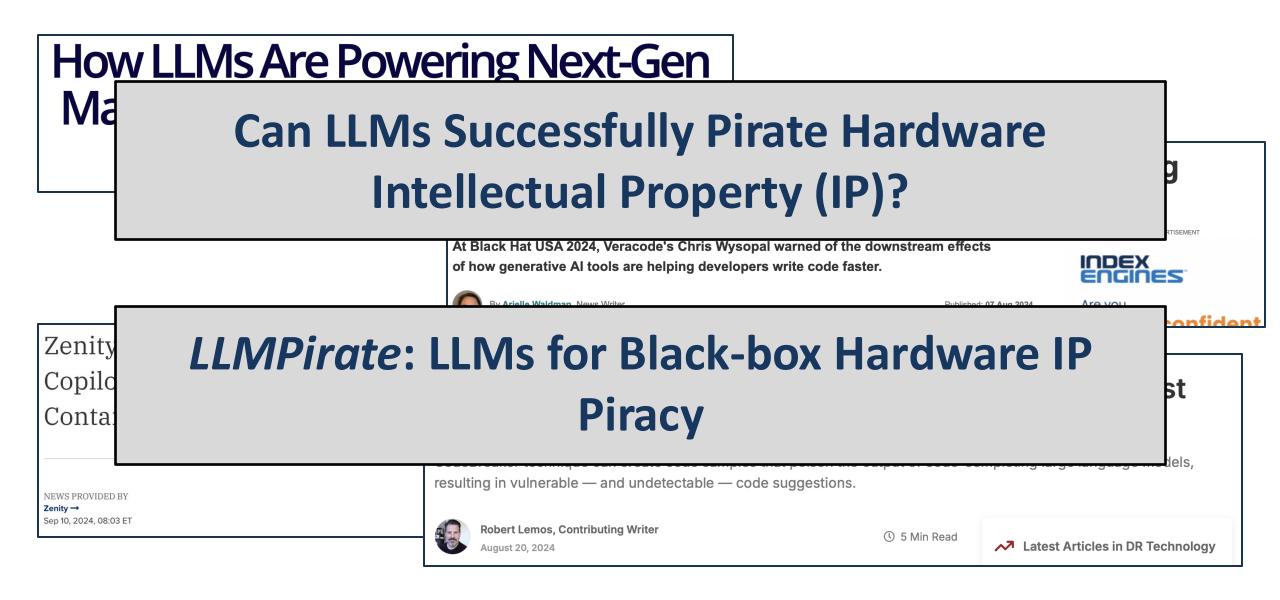
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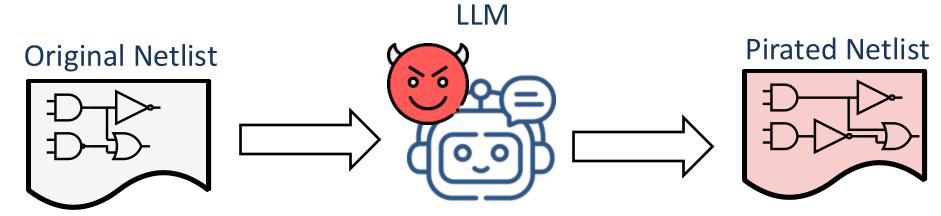


Threat Model



No Modifications

Threat Model

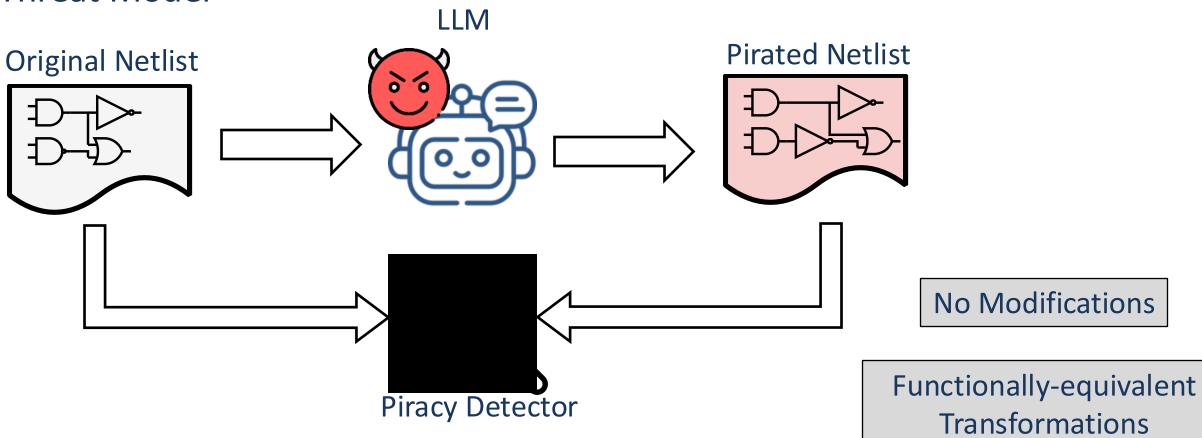




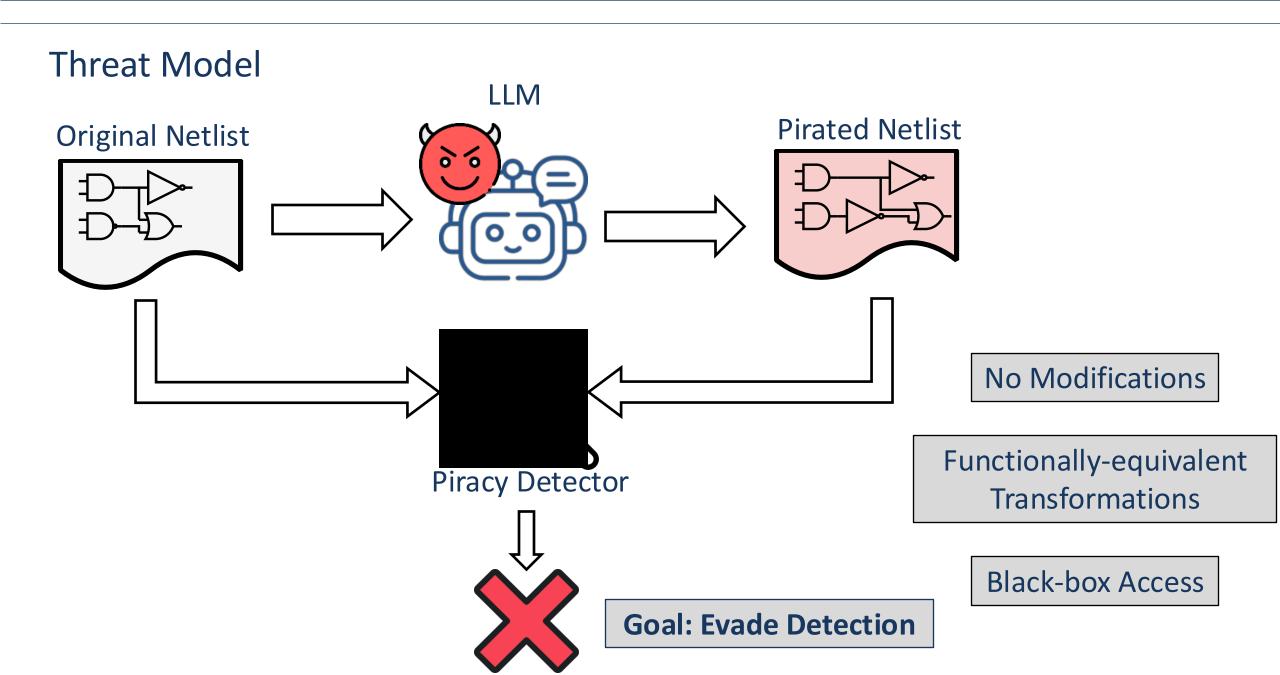
No Modifications

Functionally-equivalent Transformations

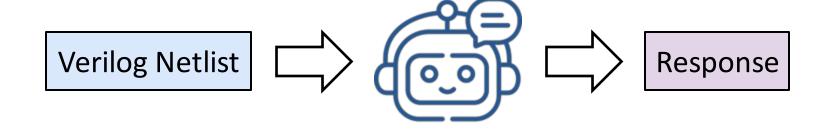
Threat Model



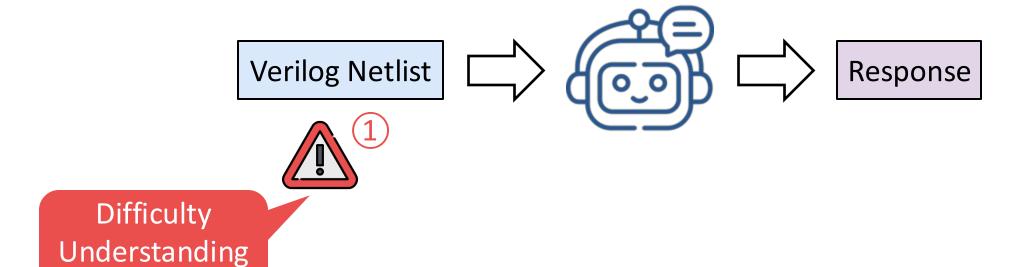
Black-box Access

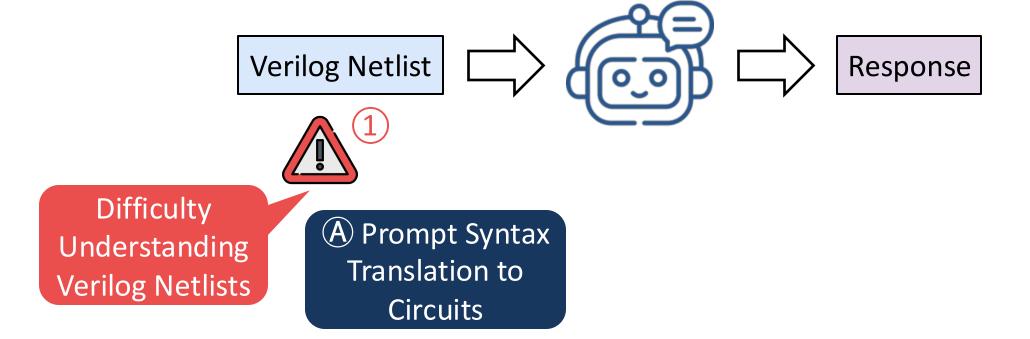


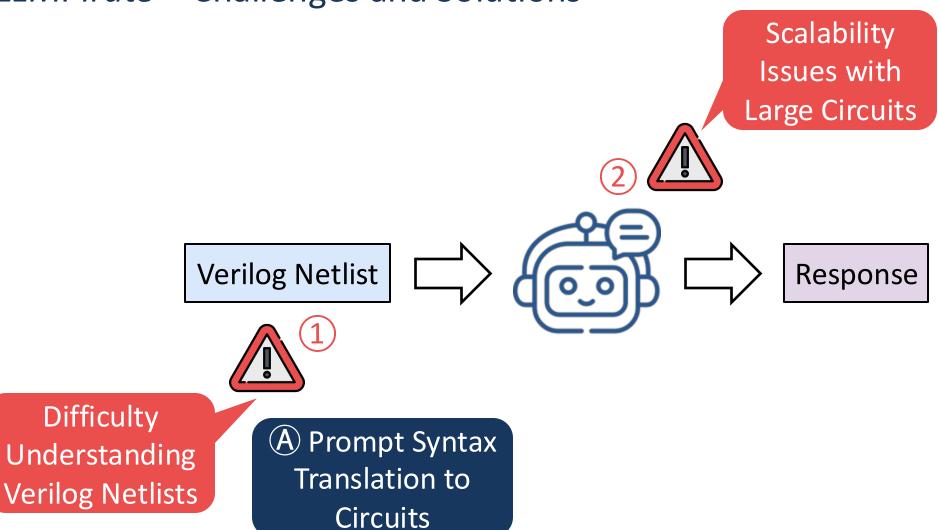
LLMPirate – Preliminary Formulation



Verilog Netlists

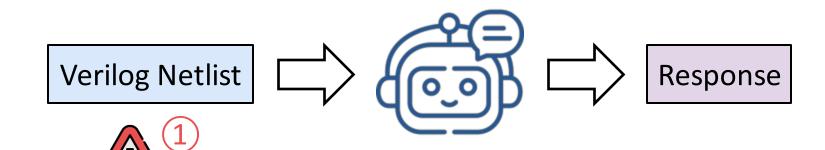






Scalability
Issues with
Large Circuits

B Pre-characterizationand Divide-and-conquer



Difficulty
Understanding
Verilog Netlists

A Prompt Syntax Translation to Circuits

LLMPirate – Challenges and Solutions Scalability Issues with Large Circuits

Translation to

Circuits

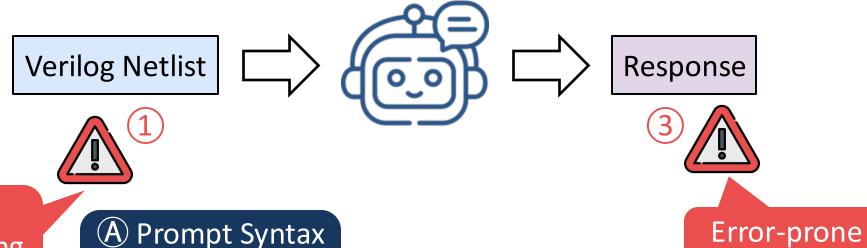
Difficulty

Understanding

Verilog Netlists

B Precharacterization and Divide-andconquer

Responses



Difficulty

Understanding

Verilog Netlists

Scalability
Issues with
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B Pre-characterizationand Divide-and-conquer

Verilog Netlist

Response

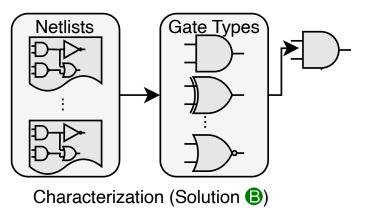
A Prompt Syntax

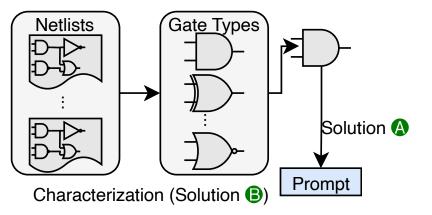
Translation to

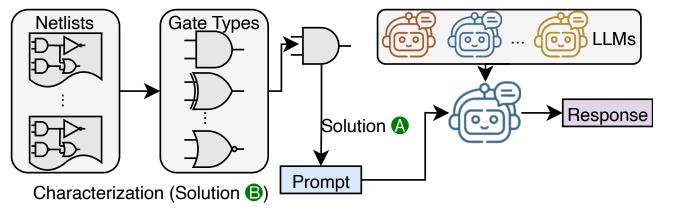
Circuits

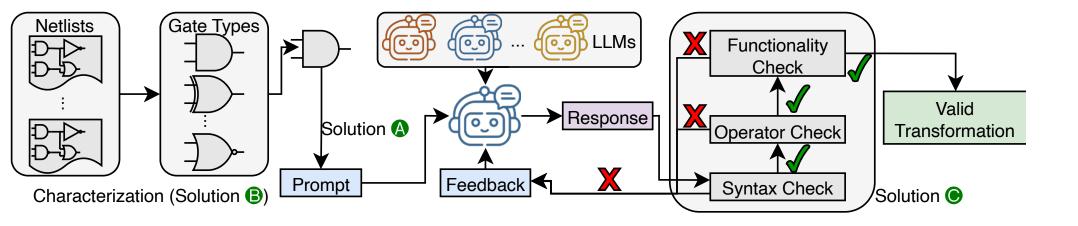
Error-prone Responses

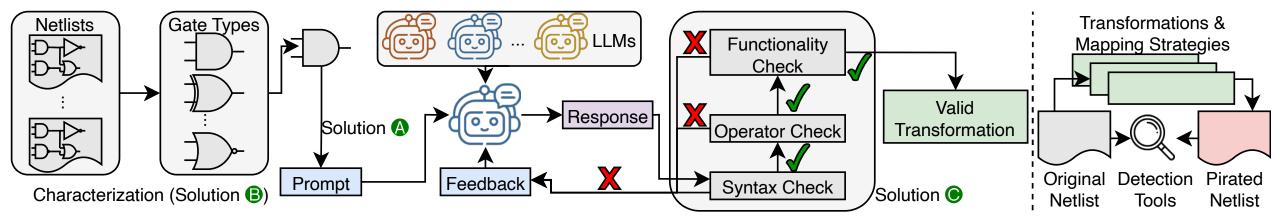
© Feedbackbased Formulation



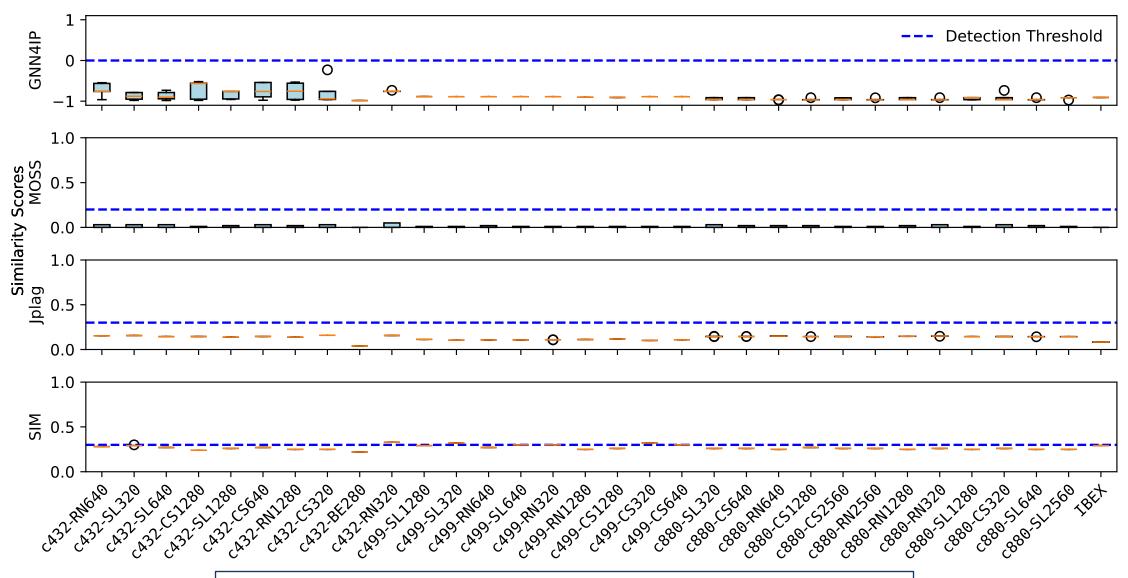








Main Results



LLMPirate Successfully Evades All Detection Tools

Ablation Study

# Successes (Sim. Score)	GNN4IP [1]	MOSS [2]	Jplag [3]	SIM [4]
LLMPirate\ Solution (A)	0 (N/A)	0 (N/A)	0 (N/A)	0 (N/A)
<i>LLMPirate</i> \ Solution 𝔻	0 (N/A)	0 (N/A)	0 (N/A)	0 (N/A)
LLMPirate\ Solution ©	32 (-0.75)	32 (0.01)	32 (0.20)	7 (0.32)
LLMPirate	32 (-0.88)	32 (0.01)	32 (0.13)	26 (0.27)

Solution B >>> Solution C

Key Findings

Model Size Matters

GPT-4 and CoPilot achieve the best performance in successfully pirating netlists

CodeLlama-13B performs significantly better than the smaller CodeLlama-7B

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Latest version of Llama (Llama3-8B) outperforms the older Llama2 models

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Feedback Improves Performance

With proper feedback and multiple attempts, smaller LLMs correct their mistakes

Thank You

Vasudev Gohil vasudevgohil.com

References

- [1] Yasaei, Rozhin, Shih-Yuan Yu, Emad Kasaeyan Naeini, and Mohammad Abdullah Al Faruque. "GNN4IP: Graph neural network for hardware intellectual property piracy detection." In 2021 58th ACM/IEEE Design Automation Conference (DAC), pp. 217-222. IEEE, 2021.
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